

Our latest news!

A. The creation of the decoder MTD on modern PLIS for speeds 50-250 Mbit/s with a code gain (CG) 6,5 - 8 dB is successfully finished. It is a simple scheme without usage of concatenation, that has allowed to realize for MTD a very simple and effective synchronizer circuit.

Is it necessary **faster and more**?

- Welcome!

Do you want **to see work of MTD** on a bench?

- Come to us!

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B. Next time the program MTD version is advanced. Now at different levels of coding redundancy it needs only 50-300 operations per bit to receive CG, more than at 3 dB superior CG at usage of standard Viterbi algorithm. This MTD has been realized in a special TV system and in general it has appeared outside of a any competition.

Is **superfast microprocessor MTD** necessary to you for speed 2-15 Mbit/s and more?

- We have it!

We can **show you such a program work** even with a customary personal computer.

- We are ready.

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C. The creation of new series of program and hardware-software simulators of satellite channels with program versions of the best known algorithms of decoding is finished. Now special compact benches with means of hooking up and demonstrating work of hardware MTD, realized on modern chips PLIS in a broad band of signal-to-noise ratios and processing speeds are included in simulators.

Are the modern **simulators of satellite channels** necessary for you?

- Just we have it.

Do you want to see the characteristics of **the best coding equipment** on a bench?

- You may come and order it.